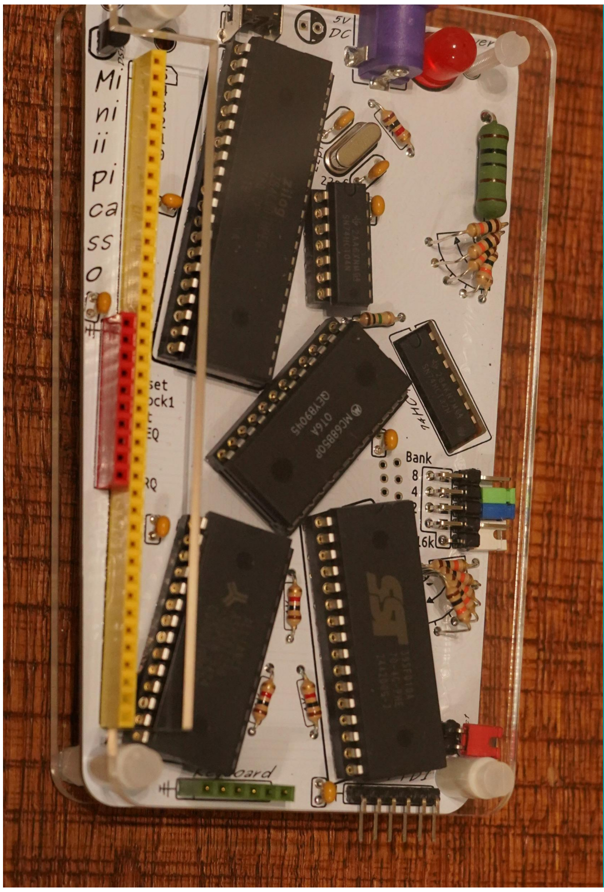
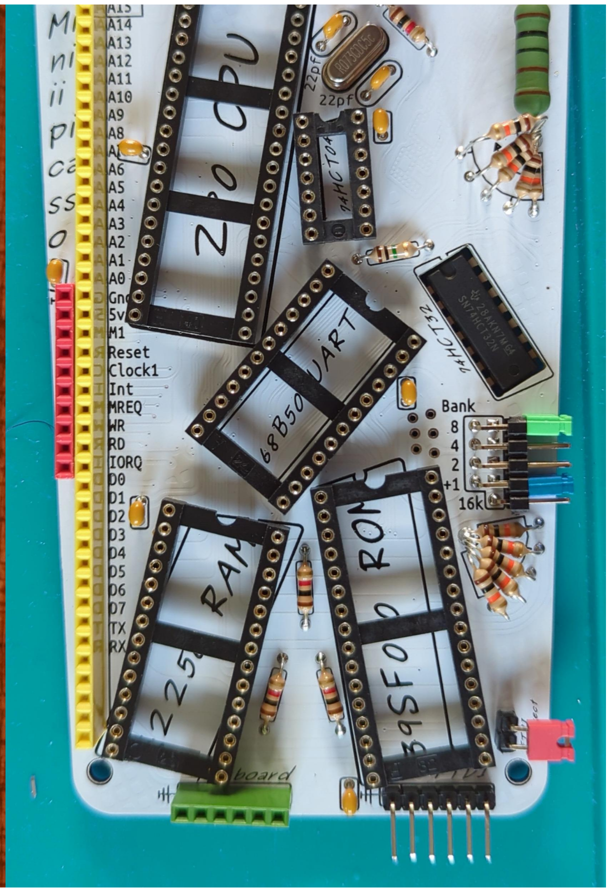
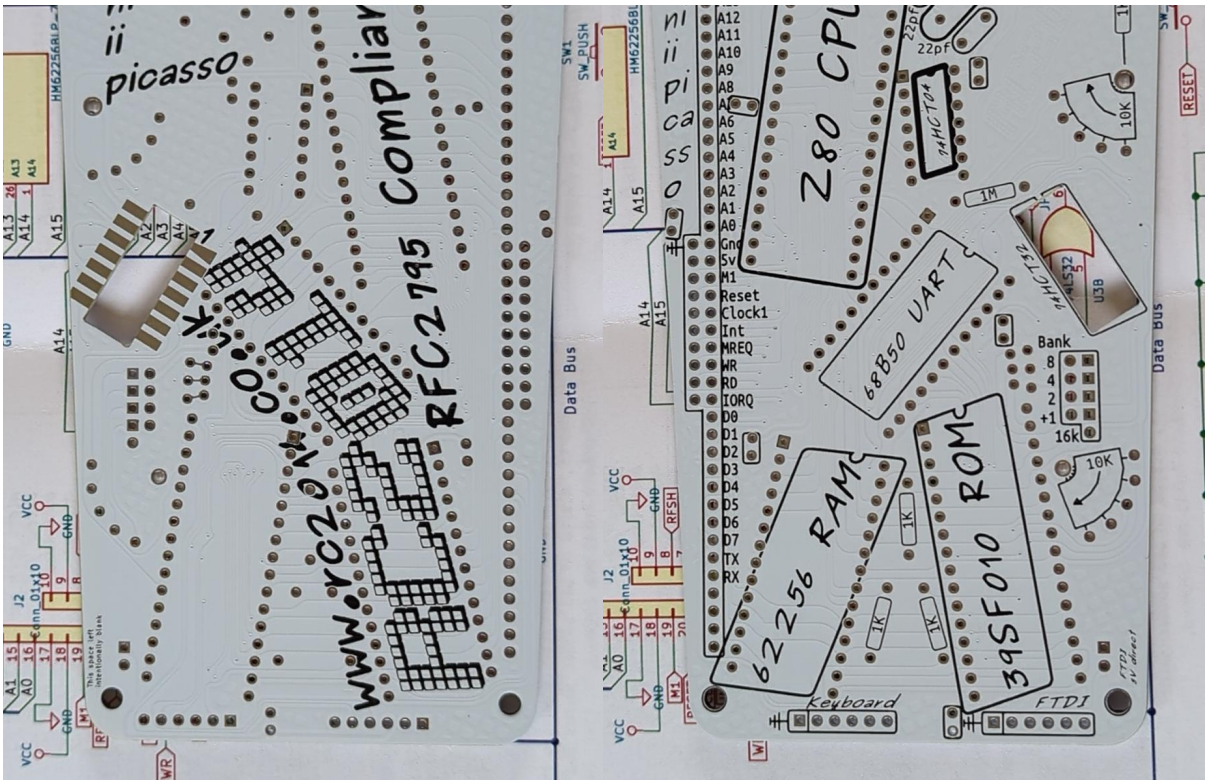
Mini II Picasso

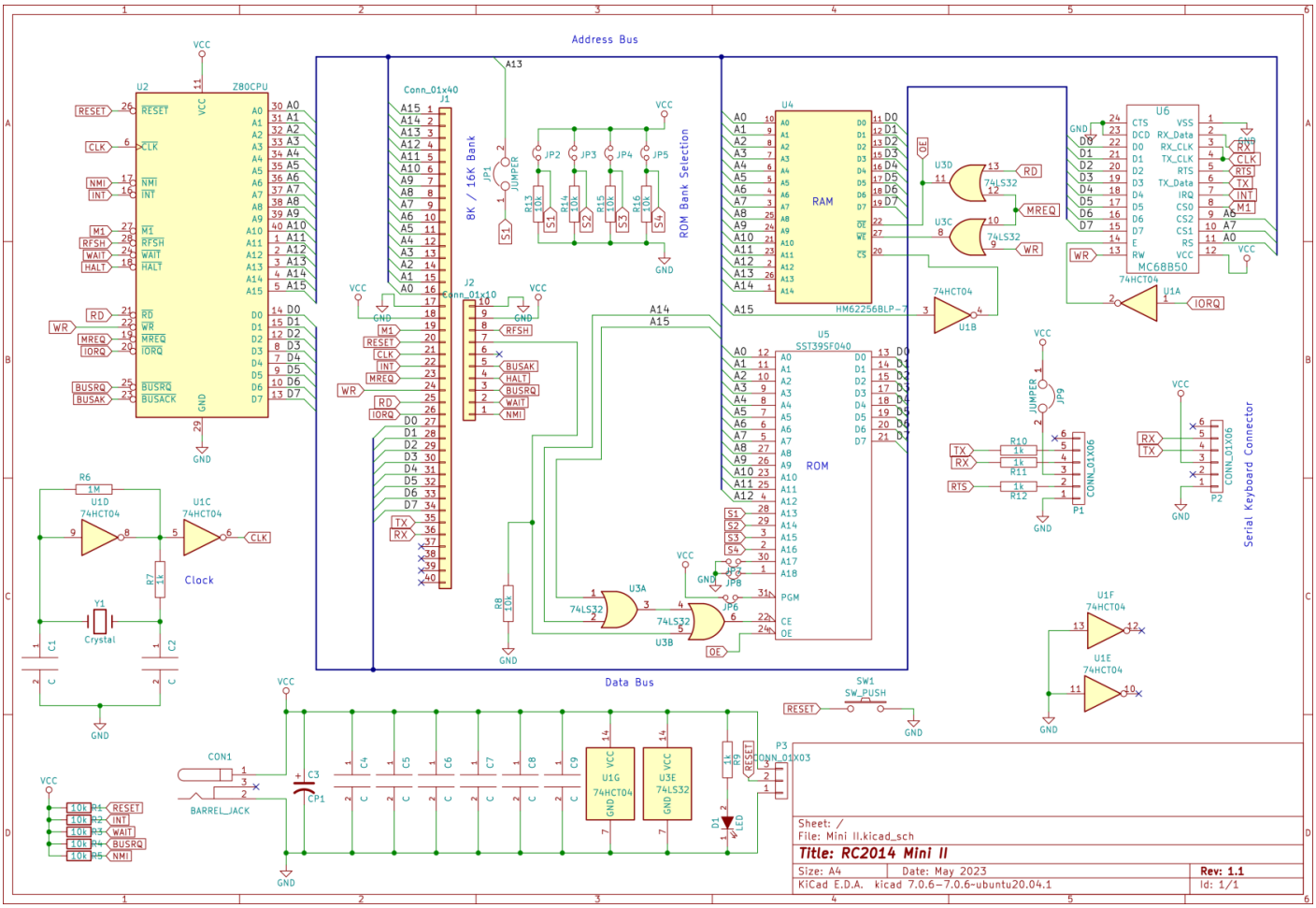
- Kit number: **942527**
- RC2014 Mini II Picasso
- RC2014 Mini II (of which Picasso is a PCB variant)


5V DC
Bank
8
4
16k


A15
A14
A13
A12
A11
A10
A9
A8
A6
A5
A4
A3
A2
A1
A0
Reset
Clock1
Int
MREQ
WR
RD
IORQ
D0
D1
D2
D3
D4
D5
D6
D7
TX
RX
22pf
22pf
Z80 CPU
74HC04
74HC32
68B50 UART
RAM
Bank
8
4
2
+1
16k


picasso
RFC2795
Z80 CPU
68B50 UART
62256 RAM
39SF010 ROM
Keyboard
FTDI
Data Bus
Bank
Reset
Clock1
Int
MREQ
WR
RD
IORQ
RESET

Schematic



> A DS1233+5 will also keep the Z80 in reset state on power-on until the voltages have stabilised, meaning the Z80 should always boot cleanly.

Memory map

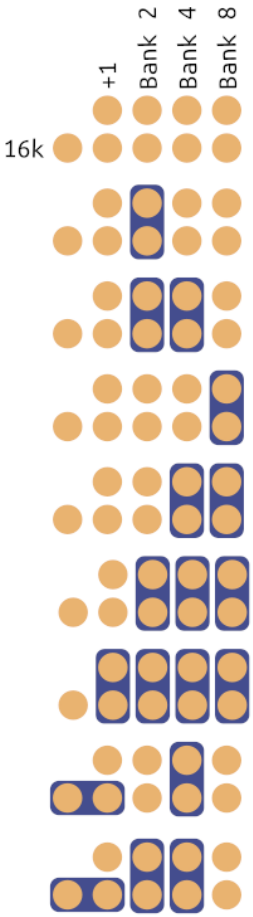
`0x0000` (`0 KiB`) - `0x1FFF`	ROM - 8 KiB Bank	ROM - 16 KiB Bank
`0x2000` (`8 KiB`) - `0x3FFF`	ROM Shadow - 8 KiB	
`0x4000` (`16 KiB`) - `0x7FFF`	*Unused*	
`0x8000` (`32 KiB`) - `0x8000`	RAM - 32 KiB	

ROM map

There is 128 KiB of ROM mapped as 8 KiB or 16 KiB bank depending on jumper setting.

> JP1/JP2 can either be horizontal (16k), omitted (lower 8k) or vertical (upper 8k).

16 KiB Bank	**8 KiB Bank**	**+1 (`--` is 16k and +1)**	**Bank 2, 4, 8**	**Image**
0	**0**	`0`	`000`	Microsoft BASIC (Phillip Stevens)
	1	`1`	`000`	*Reserved*
1	**2**	`0`	`100`	Microsoft BASIC (Phil Green)
	3	`1`	`100`	*Reserved*
2	4	`--`	`010`	CP/M* (Grant Searle)
	5			
3	6	`--`	`110`	SCM R4 inc BASIC and CP/M* (Steven Cousins)
	7			
4	**8**	`0`	`001`	CamelForth (Justin Skists)
	9	`1`	`001`	*Reserved*
5	10	`0`	`101`	*Reserved*
	11	`1`	`101`	*Reserved*
6	**12**	`0`	`011`	Microsoft BASIC (Grant Searle)
	13	`1`	`011`	*Reserved*
7	**14**	`0`	`111`	SCM R1 (Steven Cousins)
	15	`1`	`111`	SCM R1 (Steven Cousins)



8k Bank 0 Microsoft BASIC (Phillip Stevens)

8k Bank 2 Microsoft BASIC (Phil Green)

8k Bank 6 SCM R4 (Steven Cousins)

8k Bank 8 CamelForth (justin Skists)

8k Bank 12 Microsoft BASIC (Grant Searle)

8k Bank 14 SCM R1 (Steven Cousins)

8k Bank 15 SCM R1 (Steven Cousins)

16k Bank 2 CP/M* (Grant Searle)

16k Bank 3 SCM R4 inc BASIC and CP/M* (Steven Cousins)

* CP/M Requires additional hardware

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